Recent Developments in Logic-Based Methods for Optimization

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Logic and Optimization

- Boole's probability logic and linear programming
- Decision diagrams and optimization
- Predicate logic and integer programming
- Resolution and cutting planes
- Logic and duality
- Consistency and backtracking

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Focus on decision diagrams due to possible synergy with quantum computation.



George Boole is best known for Boolean logic.

But he proposed a strikingly original formulation of **reasoning under uncertainty**...

...probability logic.

It was forgotten or ignored for over 100 years.



In 1970s, Theodore Hailperin showed that probability logic poses a **linear programming** problem.

He sees this as implicit in Boole's own work.

The idea was re-invented by AI community in 1980s.



Hailperin 1976, 1984, 1986

Nilsson 1986



Statement	Probability
А	0.9
If A then B	0.8
If B then C	0.4

We can deduce C, but with what probability?

Boole's insights:

- We can only specify a **range** of probabilities for C.
- The range depends mathematically on the probabilities of **possible states of affairs** (possible worlds).

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First, interpret the if-then statements as material conditionals

Statement	Probability
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Identify the possible outcomes (possible worlds), each having an **unknown** probability.

Α	В	С	Prob.
false	false	false	p_{000}
false	false	true	<i>p</i> ₀₀₁
false	true	false	p ₀₁₀
false	true	true	p ₀₁₁
true	false	false	p ₁₀₀
true	false	true	p ₁₀₁
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$$p_{100} + p_{101} + p_{110} + p_{111} = 0.9$$

The worlds in which A is true must have probabilities that sum to 0.9.

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$$p_{000} + \ldots + p_{111} = 1$$

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Minimize and maximize probability of C:

 $p_{001} + p_{011} + p_{101} + p_{111}$

subject to these equations and $p_{ijk} \ge 0$

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Large instances solved by **column** generation.

There are linear programming models for **logics of belief and evidence** such as **Dempster-Shafer theory** and related systems.

Dempster 1968, Shafer 1976



A. P. Dempster



Glenn Shafer

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 - Goal: solve NP-hard problems to proven optimality.



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 - Goal: solve NP-hard problems to proven optimality
 - Versatile **modeling** framework (linear integer inequalities)
 - Accommodates complex **constraints**.
 - Solvers developed over decades, extremely well engineered (CPLEX, Gurobi, SCIP)
 - Solvers follow (algorithmic) Moore's Law, but reaching plateau.
 - Basic operation is solution of a linear programming relaxation (complicated)
 - SAT solvers also fast, but less versatile modeling



- **Mixed integer programming** is mainstream state of the art in combinatorial optimization
 - Basic operation is solution of a **linear programming** relaxation (complicated)
- Recent development: Discrete optimization with decision diagrams
 - Goal: solve to proven optimality



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- Goal: solve to proven optimality
- Versatile **modeling** (recursive/dynamic programming)
- Accommodates complex **constraints** (no need for linearity/convexity)
- Basic operation is solution of **shortest path problem** (very simple)
- Highly parallelizable.
- Compute shortest paths with **quantum machine**?
- Possible killer app for quantum computing?

First, some background on **decision diagrams**.



Boolean logic was forgotten for decades, except in the minds of a few logicians, including philosopher **Charles Sanders Pearce**.

Pearce saw that Boolean logic could be represented by switching circuits.



Boole 1847, 1854

Pearce 1886



C. S. Pearce

G. Boole

Claude Shannon was required to take a philosophy course while an undergraduate at University of Michigan, where he was exposed to Pearce's work.

This gave rise to his famous master's thesis, *A Symbolic Analysis of Relay and Switching Circuits*, which provided the basis of modern computing.

Shannon 1940



C. Shannon

C. Y. Lee proposed **binary-decision programs** as a means of calculating the output of switching circuits.

Lee 1959

S. B. Akers represented binary-decision programs with **binary decision diagrams**.

Akers1978

R. E. Bryant showed that **ordered BDDs** provide a unique minimal representation of a Boolean function.

Bryant 1986



Ordered BDD

There is a **unique reduced** DD representing any given Boolean function, once the variable ordering is specified.

Bryant (1986)

The reduced DD can be viewed as a branching tree with **redundancy removed**.

Superimpose isomorphic subtrees and remove redundant nodes.



Randy Bryant



1 indicates feasible solution,0 infeasible

Branching tree for 0-1 inequality $2x_0 + 3x_1 + 5x_2 + 5x_3 \ge 7$

1



Branching tree for 0-1 inequality $2x_0 + 3x_1 + 5x_2 + 5x_3 \ge 7$

Remove redundant nodes...

1



Superimpose identical subtrees...









Superimpose identical subtrees...





Superimpose identical leaf nodes...










as generated by software

BDDs have long been used for logic circuit design and product configuration.

They were recently adapted to **optimization** and **constraint programming**.

Hadžić and JH (2006, 2007)

Andersen, Hadžić, JH and Tiedemann (2007)



Tarik Hadžić



Henrik Reif Andersen



Weighted decision diagrams can represent the feasible set of an optimization problem.

- Remove paths to 0.
- Paths to 1 are feasible solutions.
- Associate costs with arcs (= weighted)
- Reduces optimization to a shortest (longest) path problem

Given a canonical distribution of arc costs (trivial to compute), Bryant's uniqueness theorem **generalizes** to weighted DDs.

JH (2013)

Maximal Stable Set Problem

(Maximal independent set problem)

Let each vertex have weight w_i Let $x_i = 1$ when vertex *i* is in stable set Select nonadjacent vertices to maximize $\sum_i w_i x_i$





Build DD with top-down compilation (unlike CS literature)

Associate a **state** with each node





Build DD with top-down compilation (unlike CS literature)

Associate a **state** with each node

хЗ

*x*4





Build DD with top-down compilation (unlike CS literature)

Associate a **state** with each node

x4

х5



Merge nodes that correspond to the same state



Merge nodes that correspond to the same state







This DD can be viewed as the **state transition graph** for a **dynamic programming** formulation of the problem.





Resulting DD is not necessarily reduced (it is in this case).

DD reduction is a more powerful simplification method than DP



Decision diagrams, multilayer neural networks, and **dynamic programming** are based on the **same principle**:

The amount of information that can be represented by the network increases exponentially with the depth.



Nonetheless, the **width** of a DD can grow **exponentially** with the size of the problem instance.

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Solution? Use **relaxed DD** of limited width. A relaxed DD represents a **superset** of feasible set.

Andersen, Hadžić, JH and Tiedemann (2007)

Nonetheless, the **width** of a DD can grow **exponentially** with the size of the problem instance.

Solution? Use **relaxed DD** of limited width.

A relaxed DD represents a **superset** of feasible set.

Relaxed DDs yield optimization bounds.

- Shortest (longest) path length is a **bound** on optimal value.
- Paradoxically, a relaxed DD that represents more solutions can be smaller.
 - Analogous to LP relaxation in IP, but **discrete**.
 - Does not require linearity, convexity, or inequality constraints.

Andersen, Hadžić, JH and Tiedemann (2007)



Use generic merging heuristics.





Use generic merging heuristics.

In this case, take union of merged states хЗ





Use generic merging heuristics.

In this case, take union of merged states *x*4

х5





Use generic merging heuristics.

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Use generic merging heuristics.

In this case, take union of merged states



х5



Use generic merging heuristics.

In this case, take union of merged states





Width = 2

Represents 11 solutions, including 9 feasible solutions





Bound from Relaxed DD

Bounds from **DD**s vs. state-of-the-art **integer programming solver** for max stable set problem

- IP solver bound relies on 50 years of experience with cutting planes
- DD max width of 1000.
- DDs require about 5% the time of IP solver

Bergman, Ciré, van Hoeve, JH (2013)



Bound from Relaxed DD

DDs normally provide bounds within a solver, but they can also provide bounds for problems solved **heuristically**.

DDs plus Lagrangian duality can provide very sharp bounds.

For example, bounds mostly within **0.1%** of best known solutions of Biskup-Feldman machine scheduling instances with time windows (never solved to optimality). Sometimes, the bounds prove optimality.

JH (2019)

Propagation through a relaxed DD can substantially improve performance of **constraint programming**.

Example: Traveling salesman problem with time windows and other sequencing problems.

DDs allowed closure of several open problem instances.

Ciré, van Hoeve (2013)

Computation time scatter plot, lex search



Performance profile, depth-first search



A **restricted** DD represents a **subset** of the feasible set. Restricted DDs provide a basis for a **primal heuristic**.

Primal heuristic = algorithm that finds solutions that are not optimal in general Primal heuristics are an important factor in recent improvements of IP solvers.

Bergman, Ciré, van Hoeve, Yunes (2014)

Optimality gap for **set covering**, *n* variables

Restricted DDs vs Primal heuristic at root node of CPLEX



Computation time

Restricted DDs vs Primal heuristic at root node of CPLEX (cuts turned off)



DDs provide a **general purpose solver** for discrete optimization.

- **Bounds** from relaxed DDs.
- **Primal heuristic** from restricted DDs.
- Recursive modeling
- Novel **branching algorithm** branch inside relaxed DD

Bergman, Ciré, van Hoeve, JH (2016)




Branching in a relaxed decision diagram



Branching in a relaxed decision diagram



Second branch

Branching in a relaxed decision diagram

3

2

1

Pruning based on **cost bounds** obtained from solving **shortest path problem** in relaxed DDs



Branching in a relaxed decision diagram

Third branch Continue recursively Pruning based on cost bounds obtained from solving shortest path problem in relaxed DDs

1

2

3

Finding **shortest (longest) path** in a relaxed DD is the **fundamental calculation**,

...much as solving an LP is the fundamental calculation in traditional MIP solvers.

Computational performance

Max cut

on a graph

Avg. solution time vs graph density

30 vertices



Computational performance

Max 2-SAT



Decompose problem into multiple DDs

Portfolio design



Bergman and Ciré (2018)

Portfolio design



Product assortment



Bergman and Ciré (2018)

Workflow employee assignment



Bergman and Ciré (2018)

Workflow employee assignment



Simplification of Dynamic Programming Models



Simplification of Dynamic Programming Models



Parallelization

Parallel computation with DDs achieves almost **100% linear speedup**. (Mixed integer programming achieves about 30% speedup)



Killer App?

Can we turn over the shortest path problem to a **quantum computer**?

- Possibly radical speedup
- Possible application of quantum computing to a wide variety of constrained combinatorial optimization problems.
- Possible massive **parallization** (solve many shortest path problemms at once).



Fundamental compactness result in 1st-order predicate logic:

Theorem (Herbrand). A formula in Skolem normal form is unsatisfiable if and only if some **finite** combination of Herbrand ground instances of its clauses is unsatisfiable.

Herbrand 1930



Jacques Herbrand

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Theorem. An IP with infinitely many constraints is infeasible if and only if some **finite** subfamily of the constraints is infeasible.



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These are the same theorem!



Jacques Herbrand

Resolution is a complete inference method for propositional logic.

Resolution:
$$x_1 \lor x_2 \lor x_4$$

 $x_1 \lor \lor \neg x_4$
 $x_1 \lor x_2$ Quine1952,1955



W. V. Quine

An **input proof** is a resolution proof in which one parent of every resolvent is among the original premises.

Cutting planes are an essential component of IP solvers.

Studied for over 50 years.

Approximate convex hull of integer solutions...

...so that LP relaxation gives tighter dual bound.

Gomory 1960, etc.





A resolvent is a **cutting plane** (rank 1 Chvátal cut)

$$\begin{array}{cccc}
x_1 + x_2 + x_4 \ge 1 & (1/2) \\
x_1 & -x_4 \ge 0 & (1/2) \\
x_2 & \ge 0 & (1/2) \\
\hline
x_1 + x_2 & \ge \lceil \frac{1}{2} \rceil
\end{array}$$

Proof of Chvátal's **cutting plane theorem** (fundamental result in cutting plane theory) implicitly relies on resolution!



V. Chvátal

Chvátal 1973

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V. Chvátal

Chvátal's cutting plane proof implicitly relies on resolution! Chvátal 1973

Theorem. The logical clauses one can infer using input proofs are precisely those that are rank 1 cuts. JH 1989

Theorem. Resolution can be generalized to a complete inference method for 0-1 inequalities (a logical analog of Chvátal's theorem).

JH 1992

An optimization problem can be viewed from two perspectives:

Primal problem: Search Dual problem: Logical inference

Search for **minimum cost** solution that satisfies constraints. Deduce from constraints the tightest possible lower **bound** on cost.

When the dual inference method is complete, the primal and dual have the same optimal value (strong duality)

Duality first described for linear programming & game theory

Coordo	Primal problem	Dual problem
	LP minimization	Also an LP, solution provides multipliers for optimality proof.
Dantzig		
	Primal problem	Dual problem
	Player 1 in 2-person noncooperative game	Player 2 in game
hn von		

John von Neumann

Optimization problems are typically solved by **primal-dual** algorithms. Search for primal and dual solution simultaneously.

All optimization duals are logical inference problems.

This implies a **tight connection** between logic and optimization.



In classical LP, the proof is a tuple of dual multipliers

Type of Dual	Inference Method	Strong?
Linear programming	Nonnegative linear combination + material implication	Yes*
Lagrangian	Nonnegative linear combination + domination	No
Surrogate	Nonnegative linear combination + material implication	No
Subadditive	Cutting planes	Yes**

*Due to Farkas Lemma **Due to Chvátal's theorem

LP Duality



Dual problem: Find the tightest lower bound on the objective function that is implied by the constraints.

LP Duality

min cx =	max v	
$Ax \ge b$	$A_{X} > b \rightarrow c_{X} > V$	
$x \ge 0$	$AX \ge D \longrightarrow CX \ge V$	

From Farkas Lemma: If $Ax \ge b$, $x \ge 0$ is feasible,

$$Ax \ge b \stackrel{x\ge 0}{\Rightarrow} cx \ge v \quad \text{iff} \quad \begin{array}{l} \lambda Ax \ge \lambda b \text{ implies } cx \ge v \\ \text{for some } \lambda \ge 0 \end{array}$$
$$\lambda A \le c \text{ and } \lambda b \ge v \end{array}$$

LP Duality



Lagrangian Duality



Lagrangian Duality



If we replace domination with material implication, we get the **surrogate dual**, which gives better bounds but lacks the nice properties of the Lagrangean dual.
Lagrangian Duality



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Lagrangian Duality



Logic, Duality and Decomposition

Often, decomposition is the key to solving large optimization problems.

- Break the problem into smaller components.
- Solution time increases **superlinearly** with component size.
- Faster to solve **many small problems** than one large one.

Components must **communicate** somehow to reach globally optimal solution.

- Decomposition can nonetheless make problem tractable,
- Given the right problem structure.



Benders Decomposition

Benders decomposition is a well-known and often successful
decomposition method.Benders 1962

Classical Benders decomposition requires an LP subproblem.

• The Benders cuts are obtained from the LP dual of the subproblem.

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Logic-based Benders decomposition accepts **any** optimization problem as the subproblem.

• Benders cuts are obtained from an inference dual of the subproblem.

Speedup over state of the art can be several orders of magnitude.

• Benders cuts must be designed specifically for every class of problems.

JH 2000

Logic and Duality



Logic-based Benders

Logic-based Benders decomposition solves a problem of the form

 $\min f(x, y)$ $(x, y) \in S$ $x \in D_x, y \in D_y$

where the problem **simplifies** when *x* is fixed to a specific value, ...usually by decoupling into small components

Logic-based Benders

Decompose problem into **master** and **subproblem**.

Subproblem is obtained by fixing x to solution value in master problem.



Logic-based Benders

Iterate until master problem value equals best subproblem value so far. Classical Benders uses LP dual of subproblem to obtain a proof.



Machine Assignment and Scheduling



Home Healthcare Routing and Scheduling



*Cuts are generated during a single branch-and-bound solution of master problem

LBBD in planning and scheduling:

- Chemical batch processing (BASF, etc.)
- Auto assembly line management (Peugeot-Citroën)
- Allocation and scheduling of multicore processors (IBM, Toshiba, Sony)
- Steel production scheduling
- Worker assignment in a queuing environment



Other scheduling applications:

- Lock scheduling
- Shift scheduling
- Permutation flow shop scheduling
- Resource-constrained scheduling
- Hospital scheduling
- Optimal control of dynamical systems
- Sports scheduling



LBBD in routing and scheduling:

- Vehicle routing
- Home health care
- Food distribution
- Automated guided vehicles in flexible manufacturing
- Traffic diversion
- Concrete delivery



LBBD in location and design:

- Allocation of frequency spectrum (U.S. FCC)
- Wireless local area network design
- Facility location-allocation
- Stochastic facility location and fleet management
- Capacity and distanceconstrained plant location
- Queuing design and control





Other LBBD applications:

- Logical inference (SAT solvers essentially use Benders!)
- Logic circuit verification
- Bicycle sharing
- Service restoration in a network
- Inventory management
- Supply chain management
- Space packing



Consistency is a core concept of constraint programming.

A **consistent partial assignment** is one that occurs in some feasible solution.

A **constraint set is consistent** if all partial assignments that violate no constraint are consistent with the constraint set.

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Various **forms** of consistency: full consistency, *k*-consistency, domain consistency.

Consistency implies less backtracking

The concept of consistency **never developed in the optimization literature**.

Yet **valid inequalities** (cutting planes) reduce backtracking by achieving a greater degree of consistency, as well as by tightening a relaxation.

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Consistency can be adapted to MILP.

Cuts that achieve consistency **cut off inconsistent 0-1 partial assignments** and so reduce backtracking.

$$S = \left\{ x_1 + 2x_2 + x_3 \le 2, \ x_j \in \{0, 1\} \right\}$$



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Questions? Comments?

